

Design and Simulation of “Maximum Gain Amplifier” using AWR Microwave office

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Example

Design an amplifier for maximum gain at 4.0 GHz using single-stub matching sections. Calculate and plot the input return loss and the gain from 3 to 5 GHz. The GaAs FET has the following S parameters ($Z_0 = 50 \Omega$):

f (GHz)	S_{11}	S_{21}	S_{12}	S_{22}
3.0	$0.80 \angle -89^\circ$	$2.86 \angle 99^\circ$	$0.03 \angle 56^\circ$	$0.76 \angle -41^\circ$
4.0	$0.72 \angle -116^\circ$	$2.60 \angle 76^\circ$	$0.03 \angle 57^\circ$	$0.73 \angle -54^\circ$
5.0	$0.66 \angle -142^\circ$	$2.39 \angle 54^\circ$	$0.03 \angle 62^\circ$	$0.72 \angle -68^\circ$

Step-1

$$\Delta = S_{11}S_{22} - S_{12}S_{21} = 0.488 \angle -162^\circ,$$

$$K = \frac{1 - |S_{11}|^2 - |S_{22}|^2 + |\Delta|^2}{2|S_{12}S_{21}|} = 1.195.$$

Shows that transistor is “*Un-conditionally stable*”

Step-2

$$\Gamma_S = \frac{B_1 \pm \sqrt{B_1^2 - 4|C_1|^2}}{2C_1} = 0.872 \angle 123^\circ$$

$$\Gamma_L = \frac{B_2 \pm \sqrt{B_2^2 - 4|C_2|^2}}{2C_2} = 0.876 \angle 61^\circ.$$

Step-3

$$G_S = \frac{1}{1 - |\Gamma_S|^2} = 4.17 = 6.20 \text{ dB},$$

$$G_0 = |S_{21}|^2 = 6.76 = 8.30 \text{ dB},$$

$$G_L = \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2} = 1.67 = 2.22 \text{ dB}.$$

$$G_T = \frac{1 - |\Gamma_S|^2}{|1 - \Gamma_{IN}\Gamma_S|^2} |S_{21}|^2 \frac{1 - |\Gamma_L|^2}{|1 - S_{22}\Gamma_L|^2}$$

$$G_{T_{\max}} = 6.20 + 8.30 + 2.22 = 16.7 \text{ dB}.$$

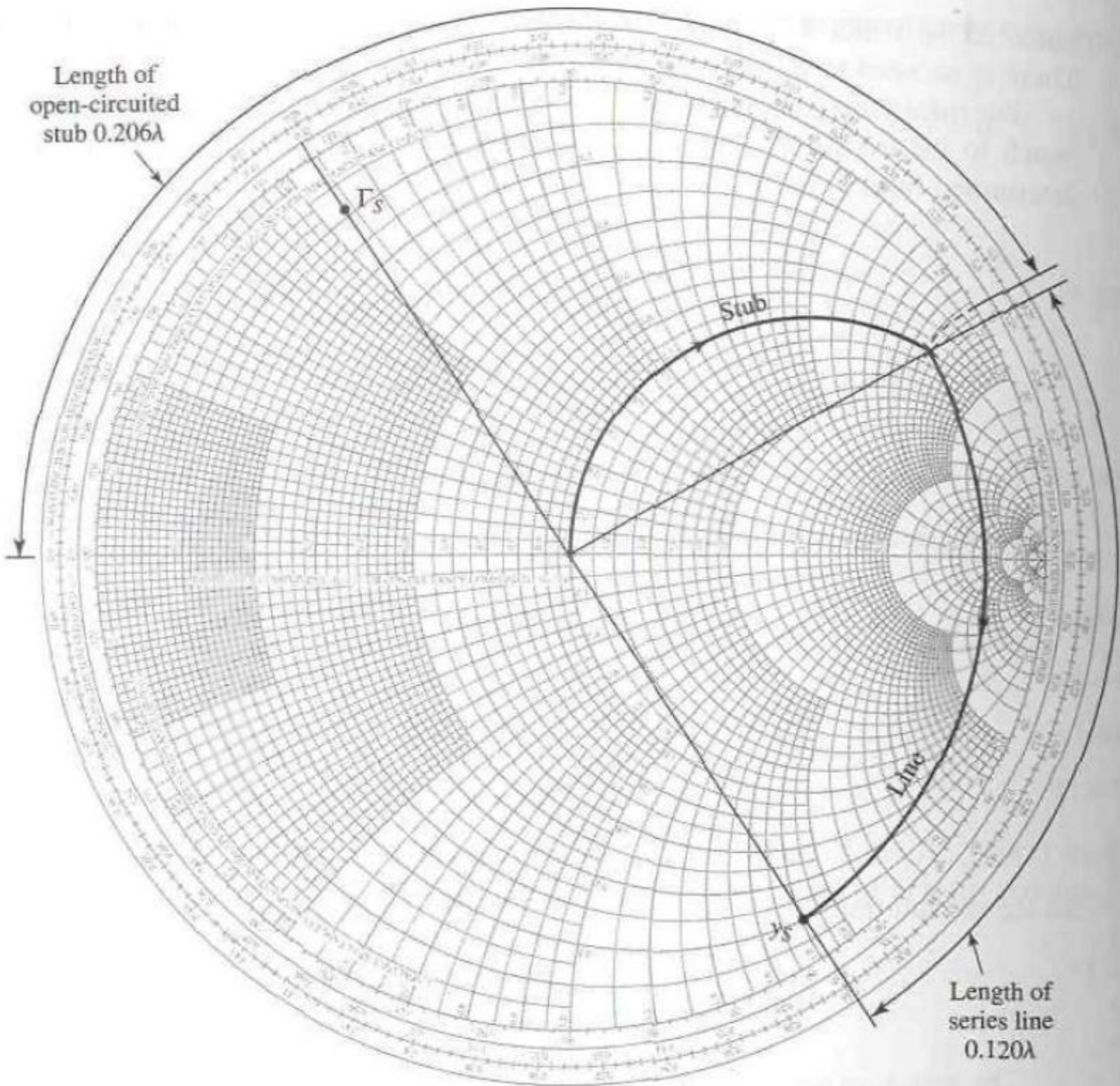
Step-4: For that calculated Γ_S , Γ_L values (in the stable region), we have to determine the matching circuit.

There are several ways to design the “matching circuit”, Single stub matching is widely used for amplifier design because of the effectiveness.

More over “open circuited stub” are highly preferred for its many advantages.

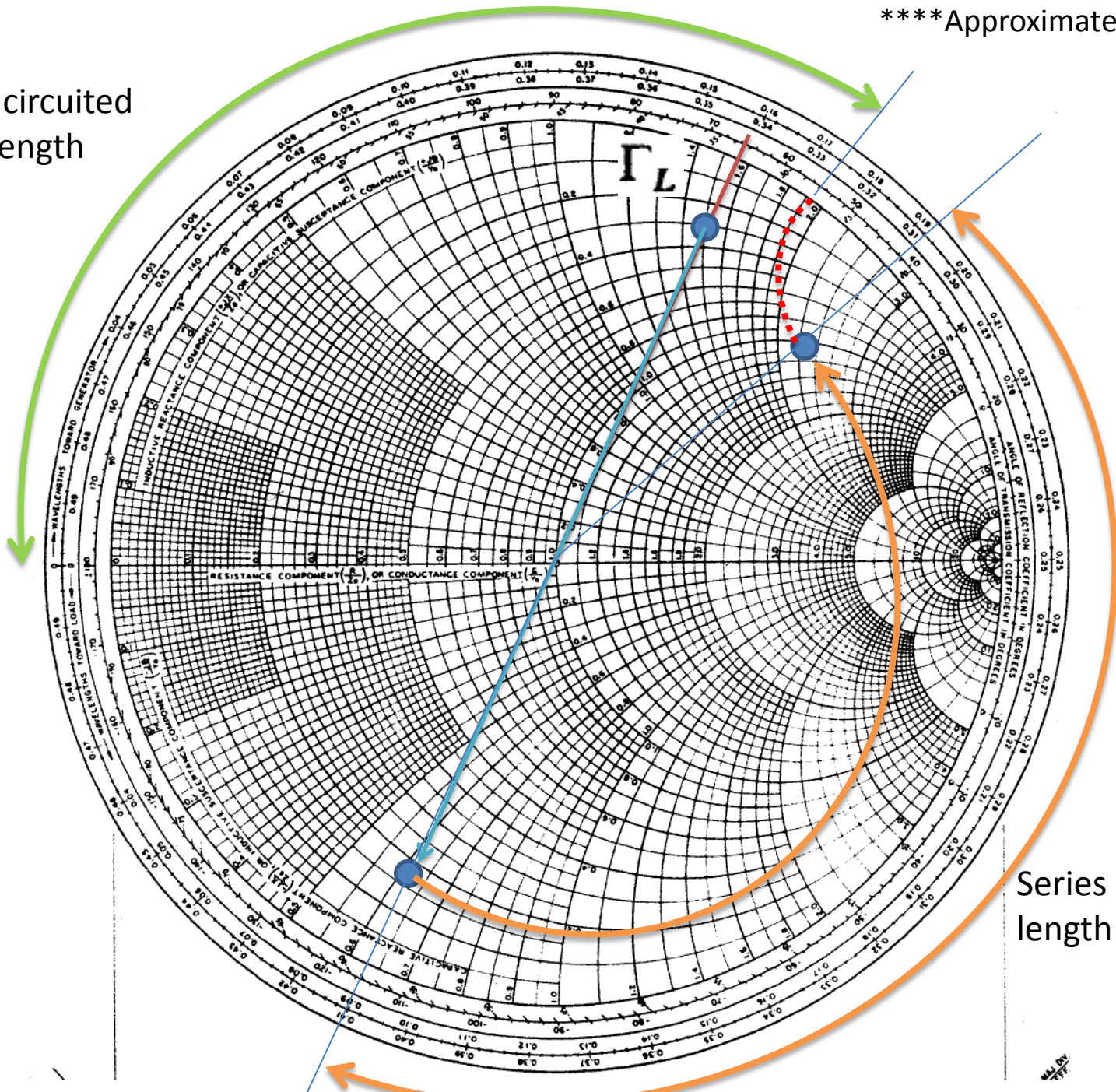
$$\Gamma_S = \frac{B_1 \pm \sqrt{B_1^2 - 4|C_1|^2}}{2C_1} = 0.872 \angle 123^\circ$$

$$\Gamma_L = \frac{B_2 \pm \sqrt{B_2^2 - 4|C_2|^2}}{2C_2} = 0.876 \angle 61^\circ.$$

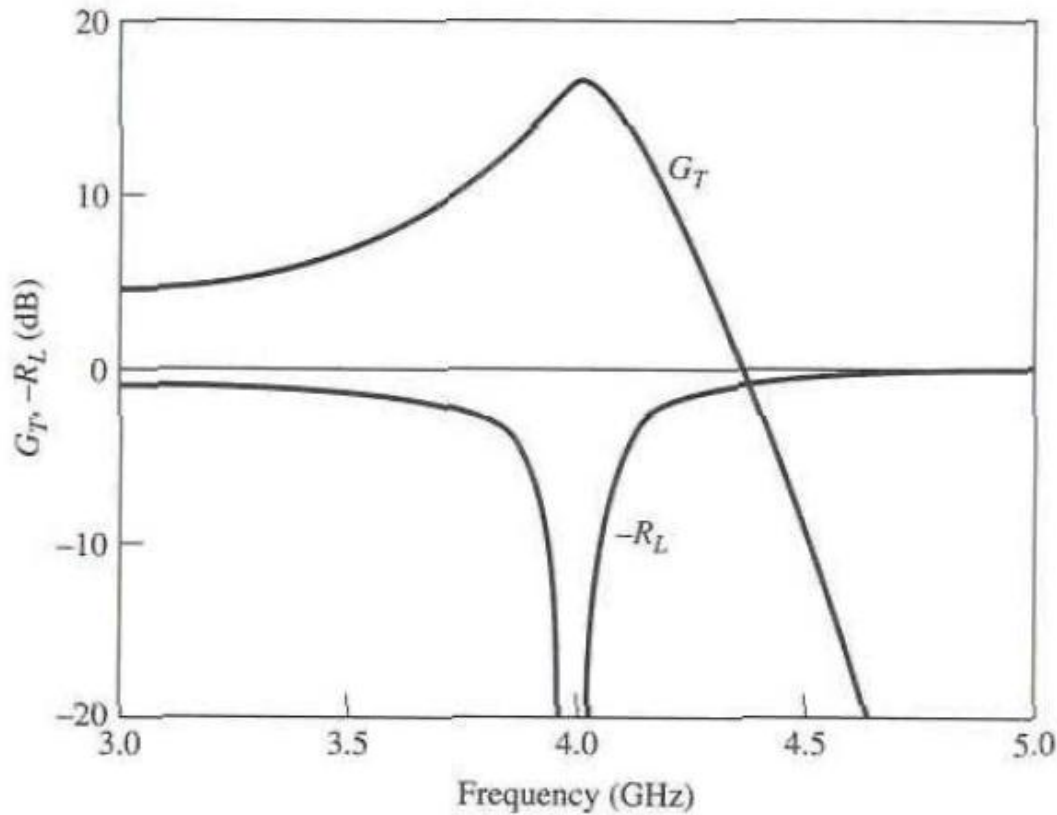
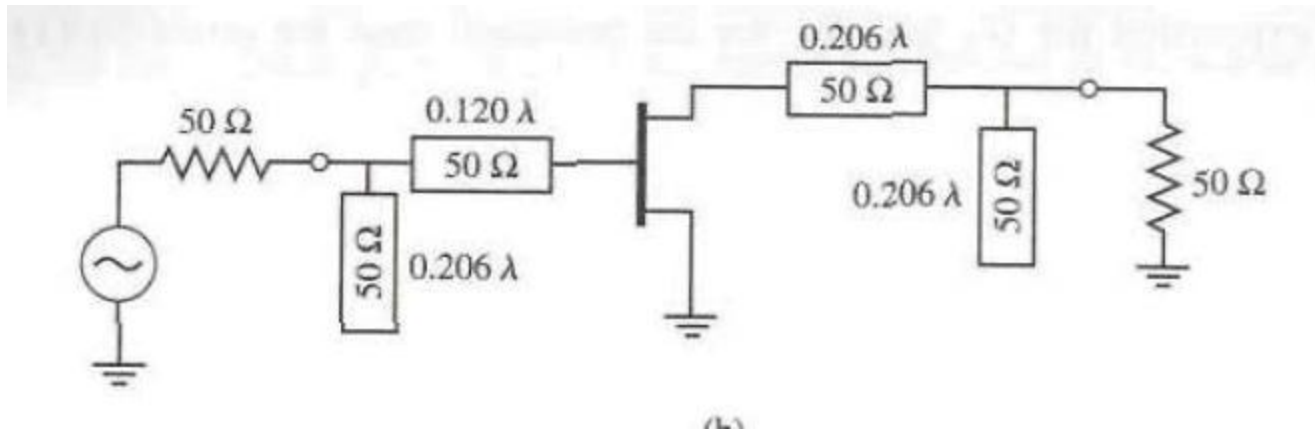


**** Approximated calculations

Open circuited
Stub length



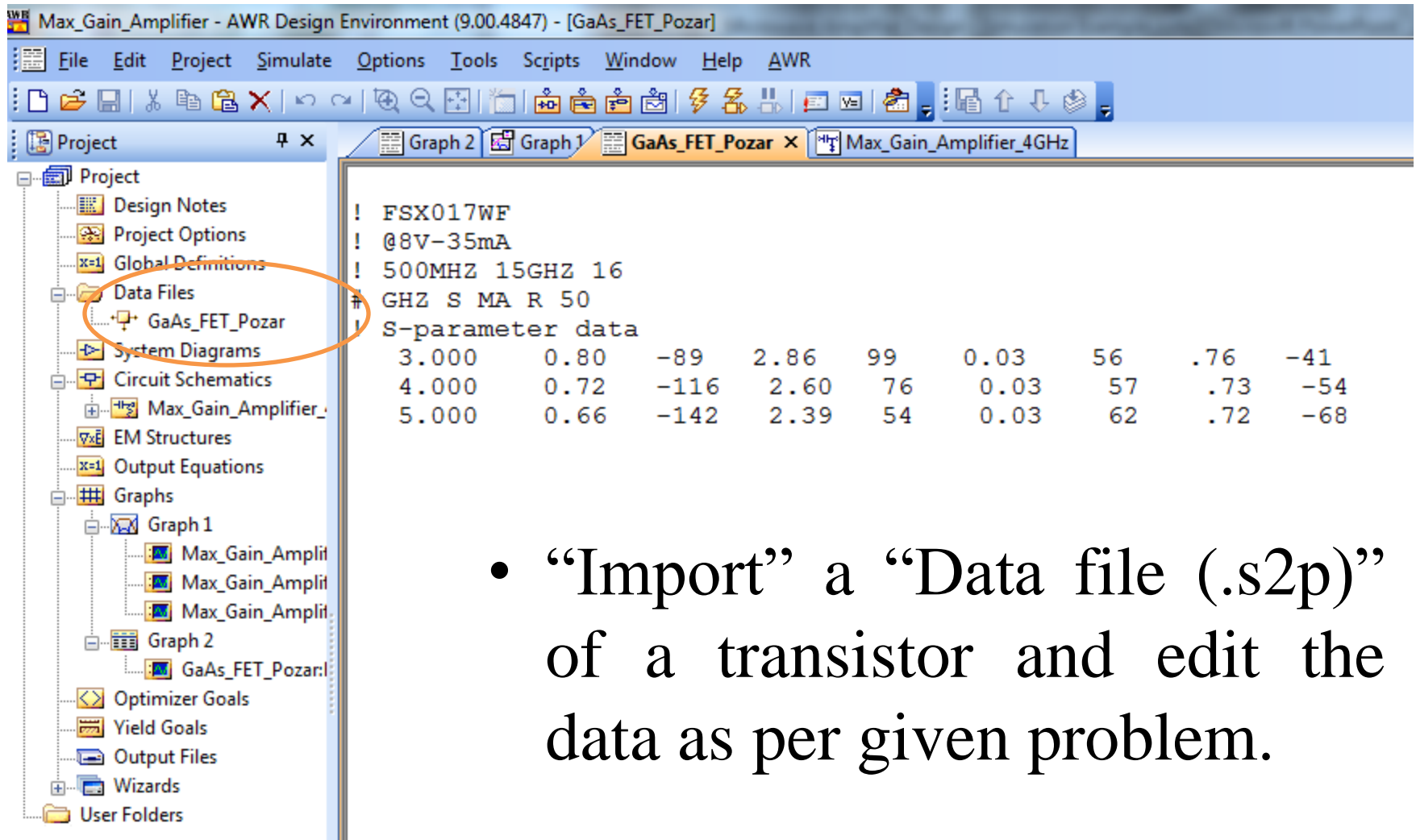
Series line
length



$$0.120 \times 360^\circ = 43.2^\circ$$

$$0.206 \times 360^\circ = 74.16^\circ$$

Simulation



The screenshot shows the AWR Design Environment interface. The left pane displays a project tree with the following structure:

- Project
 - Design Notes
 - Project Options
 - Global Definitions
 - Data Files
 - GaAs_FET_Pozar
 - System Diagrams
 - Circuit Schematics
 - Max_Gain_Amplifier_
 - EM Structures
 - Output Equations
 - Graphs
 - Graph 1
 - Max_Gain_Amplit
 - Max_Gain_Amplit
 - Max_Gain_Amplit
 - Graph 2
 - GaAs_FET_Pozar:
 - Optimizer Goals
 - Yield Goals
 - Output Files
 - Wizards
 - User Folders

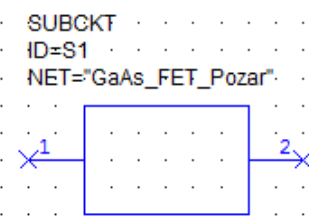
The right pane displays the following text:

```
! FSX017WF
! @8V-35mA
! 500MHZ 15GHZ 16
# GHZ S MA R 50
! S-parameter data
3.000 0.80 -89 2.86 99 0.03 56 .76 -41
4.000 0.72 -116 2.60 76 0.03 57 .73 -54
5.000 0.66 -142 2.39 54 0.03 62 .72 -68
```

- “Import” a “Data file (.s2p)” of a transistor and edit the data as per given problem.

- Interconnects
- Linear Devices
- Lumped Element
- MeasDevice
- Microstrip
- Nonlinear
- PRE_RELEASE
- Ports
- Simulation Control
- Sources
- Stripline
- Substrates
- Transmission Lines
- Waveguide
- Subcircuits
- System Blocks

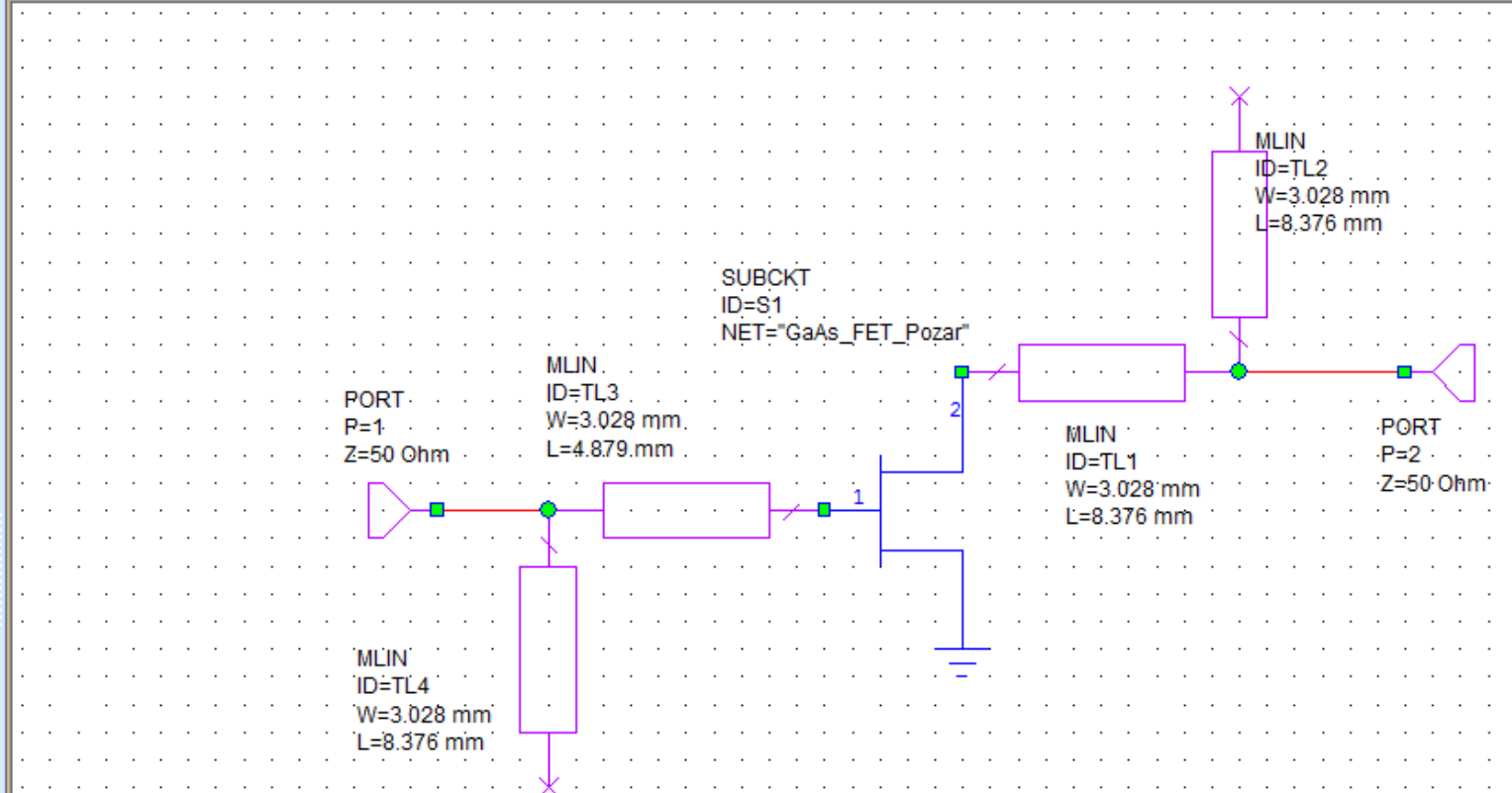
- GaAs_FET_...
- Max_Gain_...



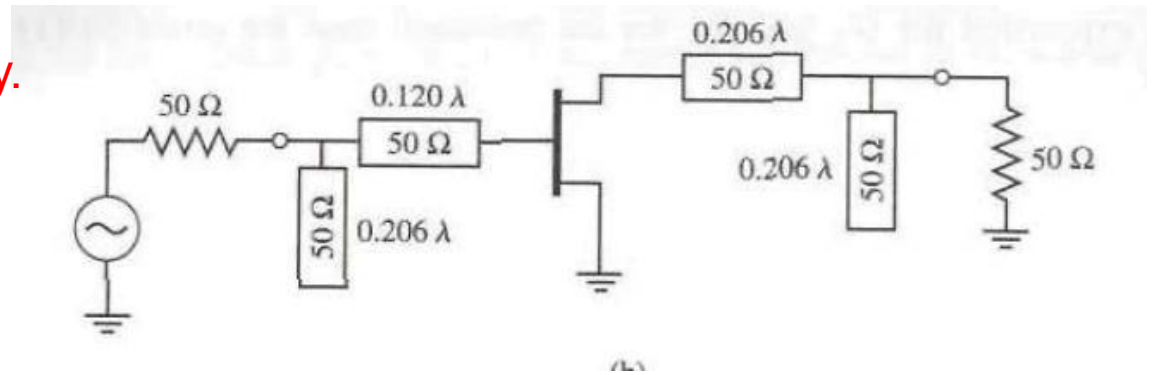
Right click on this block and "properties". Change the Symbol to "FET" (FET2@system.syf)

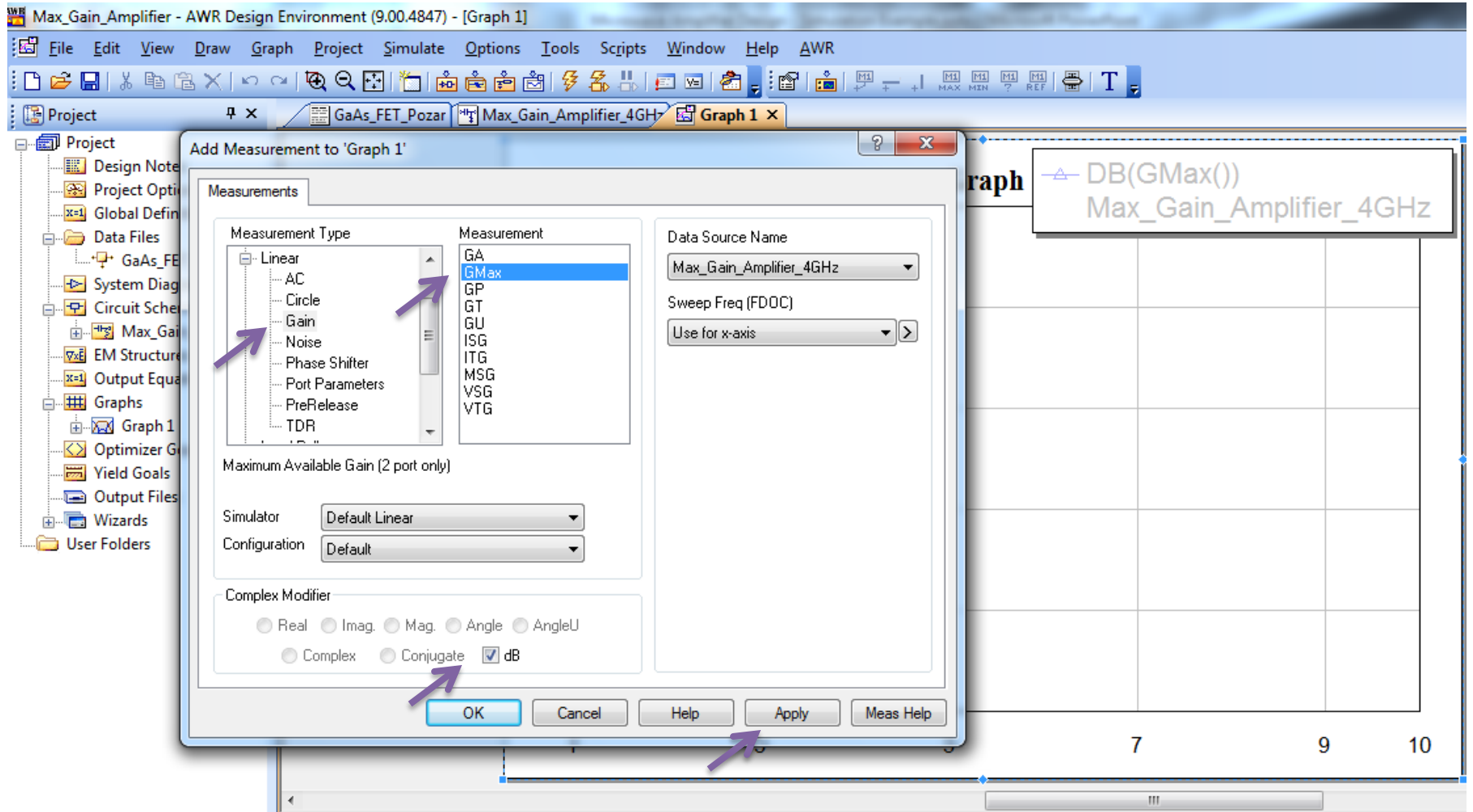
Project

- Design Notes
- Project Options
- Global Definitions
- Data Files
 - GaAs_FET_Pozar
- System Diagrams
- Circuit Schematics
 - Max_Gain_Amplifier_
- EM Structures
- Output Equations
- Graphs
 - Graph 1
- Optimizer Goals
- Yield Goals
- Output Files
- Wizards
- User Folders



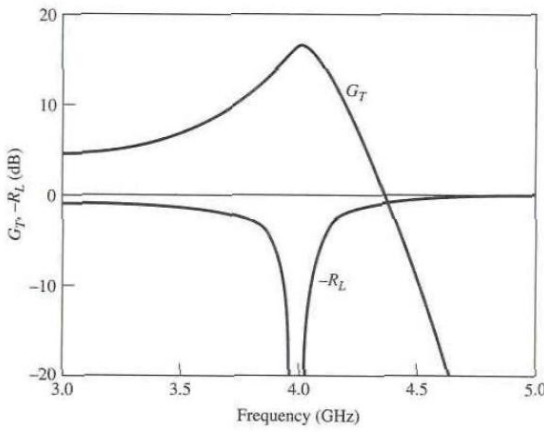
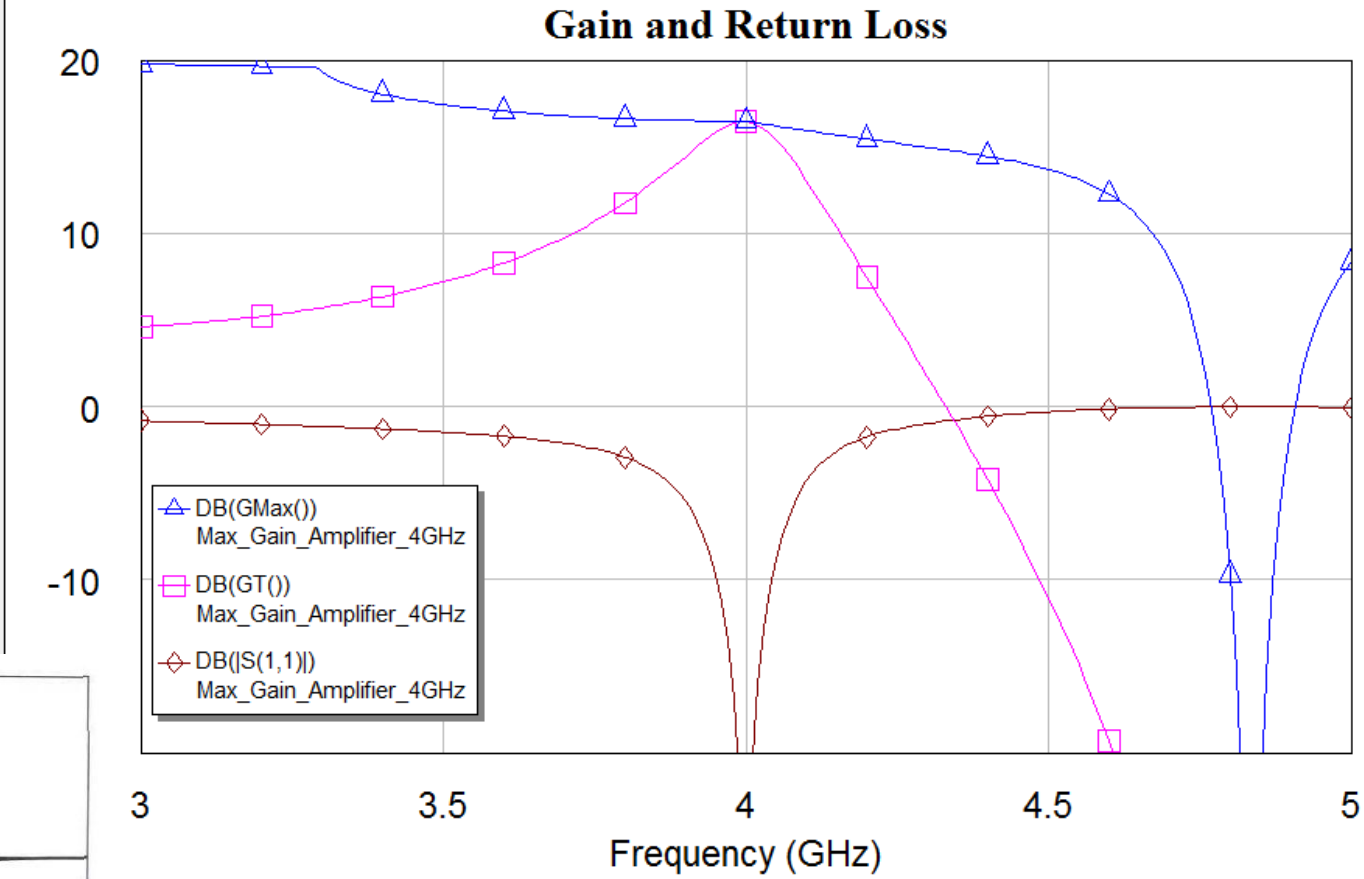
Place the “series lines” and “Open circuited stubs” properly. Also add “Substrate”





Project

- Design Notes
- Project Options
- Global Definitions
- Data Files
 - GaAs_FET_Pozar
- System Diagrams
- Circuit Schematics
- EM Structures
- Output Equations
- Graphs
 - Gain and Return Loss
 - Max_Gain_Amplifit
 - Max_Gain_Amplit
 - Max_Gain_Amplit
 - Graph 2
 - GaAs_FET_Pozar:
- Optimizer Goals
- Yield Goals
- Output Files
- Wizards
- User Folders



$G_{max} = G_T$, which proves that our “Maximum gain amplifier” design is correct.